

1. This Office Action is responsive to the Election/Restriction filed on 4/3/08. Accordingly, the applicant elected claims 1-3.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it must be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the claims:

- Claims 4-10 are canceled.
3. Note that this application is in condition for allowance except for the presence of claim 4-10 directed to claims non-elected without traverse. Accordingly, claims 4-10 have been cancelled.

REASONS FOR ALLOWANCE

4. Claims 1-3 are allowed.
5. The following is an examiner's statement of reasons for allowance:

References Wright et al (5,990,738), Chow et al (6,614,854) and Ishikawa et al (US 2005/0047521) are cited because they are pertinent to the claim invention.

Regarding to claim 1, none of the prior art of record teaches or suggests a distortion compensating amplifier having a distortion compensator for compensating for distortion of an

amplifier by updating distortion compensation coefficients so as to null a difference between a transmit signal and a feedback signal, and subjecting the transmit signal to distortion compensation processing using the distortion compensation coefficients; an amplifier for amplifying the transmit signal, which has undergone distortion compensation, a feedback unit for inputting an output signal from the amplifier to a distortion compensation coefficient updater in the distortion compensator as the feedback signal; and a delay circuit for inputting the transmit signal to the distortion compensation coefficient updater upon delaying the transmit signal, said distortion compensating amplifier comprising: means for monitoring whether the level of an unwanted-wave signal that penetrates the amplifier is greater than a set level; and update processing suspension means for suspending processing for updating the distortion compensation coefficients if the level of the unwanted-wave signal is greater than the set level. It would not have been obvious for a person skilled in the art to combine either one or combination of Wright et al (5,990,738), Chow et al (6,614,854) and Ishikawa et al (US 2005/0047521) with other prior art of record in order to arrive at the claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sanh D. Phu whose telephone number is (571)272-7857. The examiner can normally be reached on M-Fr from 8:00-16:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nay Maung can be reached on (571) 272-7882. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sanh D Phu/
Primary Examiner
Art Unit 2618

SP